$\mathcal{JPW}$ 2183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

NEVILL et al.   Atty. Ref.:   550-241

Serial No. 09/887,522   TC/A.U.:   2183

Filed: June 25, 2001   Examiner: O'Brien, B.

For: HARDWARE INSTRUCTION TRANSLATION WITHIN A
PROCESSOR PIPELINE

      *   *   *   *   *   *   *   *   *   *   *

                               November 3, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

As suggested by 37 C.F.R. 1.97, the undersigned attorney brings to the attention of

the Patent and Trademark Office the references listed on the attached form PTO-1449.

    ☒   All listed documents are attached..

    ☐   This application was filed after June 30, 2003 so that copies of U.S. Patent

Publications are not required and are not attached.

    ☐   Listed foreign patent publications and other documents are enclosed.

    ☐   The listed documents were cited in the ISR and copies should have been

supplied by WIPO directly to the US PTO. If copies are not timely received from WIPO,

please telephone the undersigned so that copies can be timely supplied for the Examiner's

consideration in this US National Phase Application.

This is not to be construed as a representation that a search has been made or that

no better prior art exists, or that a reference is relevant merely because cited.

The Examiner is requested to initial the attached form PTO-1449 and to return a

copy of the initialed document to the undersigned as an indication that the attached

references have been considered and made of record.

A check in the amount of $180.00 is attached.

NEVILL et al.
Serial No. 09/887,522

Respectfully submitted,

NIXON & VANDERHYE P.C.

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INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO.	SERIAL NO.
	550-241	09/887,522

	APPLICANT
	NEVILL et al.

(Use several sheets if necessary)	FILING DATE	TC/A.U.
	June 25, 2001	2183

NOV 0 3 2004

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,805,895	9/1998	Breternitz, Jr.			
	3,955,180	5/1976	Hirtle			
	5,970,242	10/1999	O'Connor			
	5,619,665	4/1997	Emma			
	5,826,089	10/1998	Ireton			
	5,953,520	9/1999	Mallick			
	5,568,646	10/1996	Jaggar			
	5,758,115	5/1998	Nevill			
	5,367,685	11/1994	Gosling			

FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

	IBM Technical Disclosure Bulletin, March 1988, pp308-309, "System/370 Emulator Assist Processor For a Reduced Instruction Set Computer".
	IBM Technical Disclosure Bulletin, July 1986, pp548-549, "Full Function Series/1 Instruction Set Emulator".
	IBM Technical Disclosure Bulletin, March 1994, pp605-606, "Real-Time CISC Architecture HW Emulator On A RISC Processor".
	IBM Technical Disclosure Bulletin, March 1998, p272, "Performance Improvement Using An EMULATION Control Block".
	IBM Technical Disclosure Bulletin, January 1995, pp537-540, "Fast Instruction Decode For Code Emulation on Reduced Instruction Set Computer/Cycles Systems".
	IBM Technical Disclosure Bulletin, February 1993, pp231-234, "High Performance Dual Architecture Processor".
	IBM Technical Disclosure Bulletin, August 1989, pp40-43, "System/370 I/O Channel Program Channel Command Word Prefetch".
	IBM Technical Disclosure Bulletin, June 1985, pp305-306, "Fully Microcode-Controlled Emulation Architecture".
	IBM Technical Disclosure Bulletin, March 1972, pp3074-3076, "Op Code and Status Handling For Emulation".
	IBM Technical Disclosure Bulletin, August 1982, pp954-956, "On-Chip Microcoding of a Microprocessor With Most Frequently Used Instructions of Large System and Primitives Suitable for Coding Remaining Instructions"

*Examiner		Date Considered	

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)

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	550-241	09/887,522
	APPLICANT	
	NEVILL et al.	
(Use several sheets if necessary)	FILING DATE	TC/A.U.
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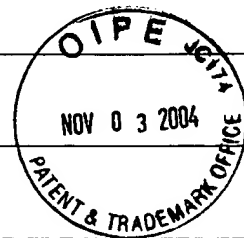
		IBM Technical Disclosure Bulletin, April 1983, pp5576-5577, "Emulation Instruction"
		ARM System Architecture by S Furber.
		Computer Architecture: A Quantitative Approach by Hennessy and Patterson; and the book The Java Virtual Machine Specification by Tim Lindholm and Frank Yellin 1st and 2nd Editions.
		The Java Virtual Machine Specification, Tim Lindholm.

*Examiner		Date Considered	

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